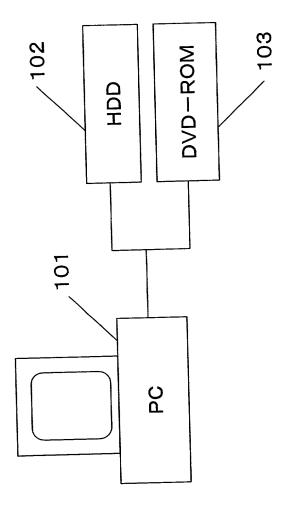
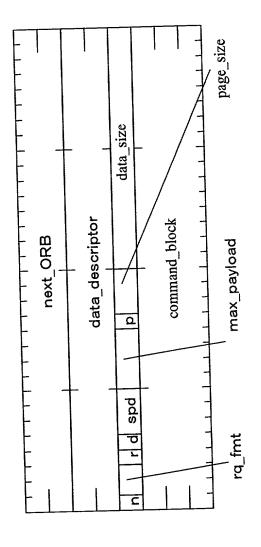
FIG. 1 RELATED ART



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FIG. 2 RELATED ART



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FIG. 3 RELATED ART

Operation_Code(28h = Read10)	Reserve		Logical Block Address		Reserve	Transfer Length		Reserve	
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FIG. 4 RELATED ART

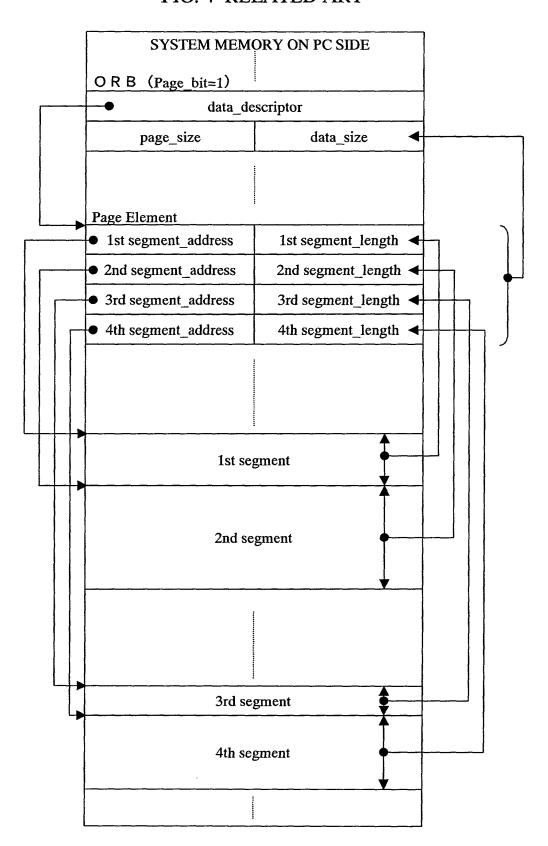
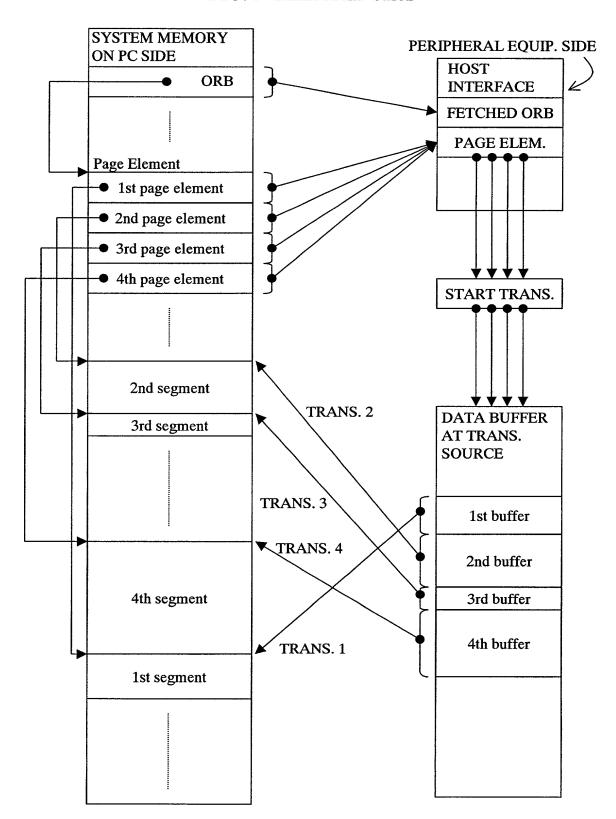
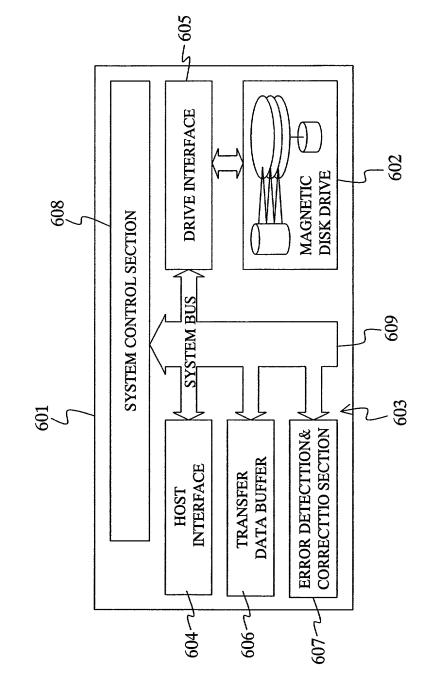


FIG. 5 RELATED ART









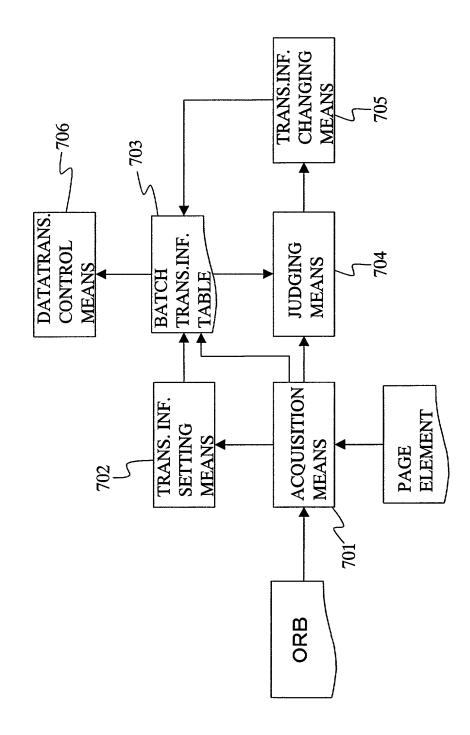


FIG. 8

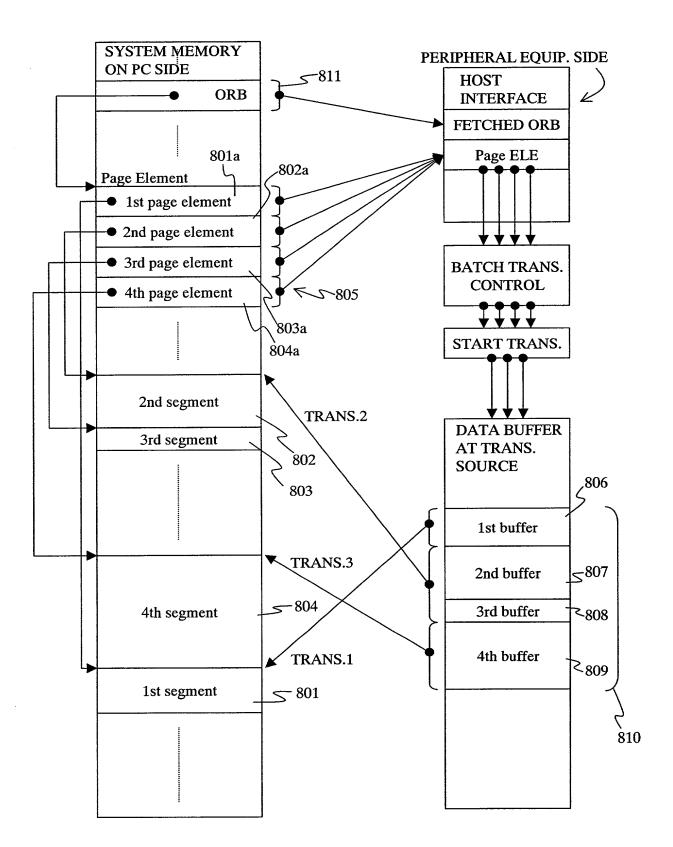


FIG. 9A

703	segment_base_hi	segment_base_lo	buffer_point	transfer length
	page_counter	segment	ealfud	transfer

FIG. 9B

/ 703

page_counter = data_size	1st segment_base_hi
1st segm	1st segment_base_lo
buffer_point=Log	buffer_point=Logical Block Address
transfer_length =	transfer length = 1st segment_length

FIG. 10A

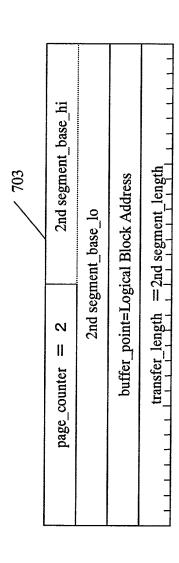
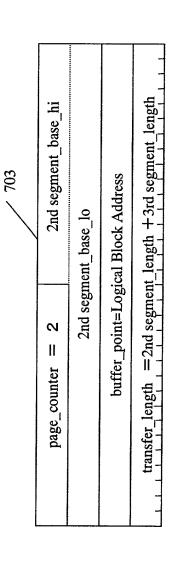


FIG. 10B



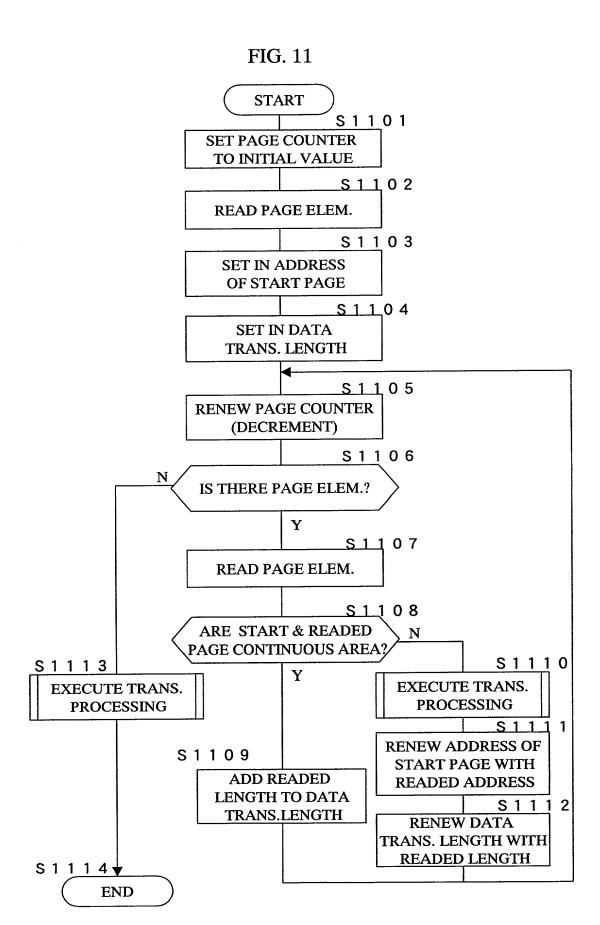


FIG. 12

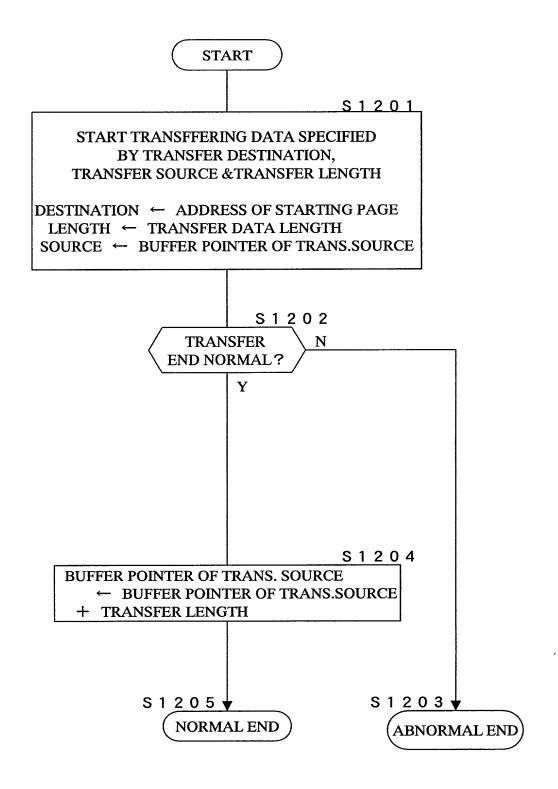


FIG. 13

703	page_counter	transfer_length	point	1st segment_base_hi	ıt_base_lo	2nd segment_base_hi	2nd segment_base_lo	 nth segment_base_hi	nt_base_lo
	end_page	transfer	buffer_point	1st segment_length	1st segment_base_lo	2nd segment_length	2nd segme	 nth segment_length	nth segment_base_lo
	start_page			1st segr		2nd seg		nth segr	

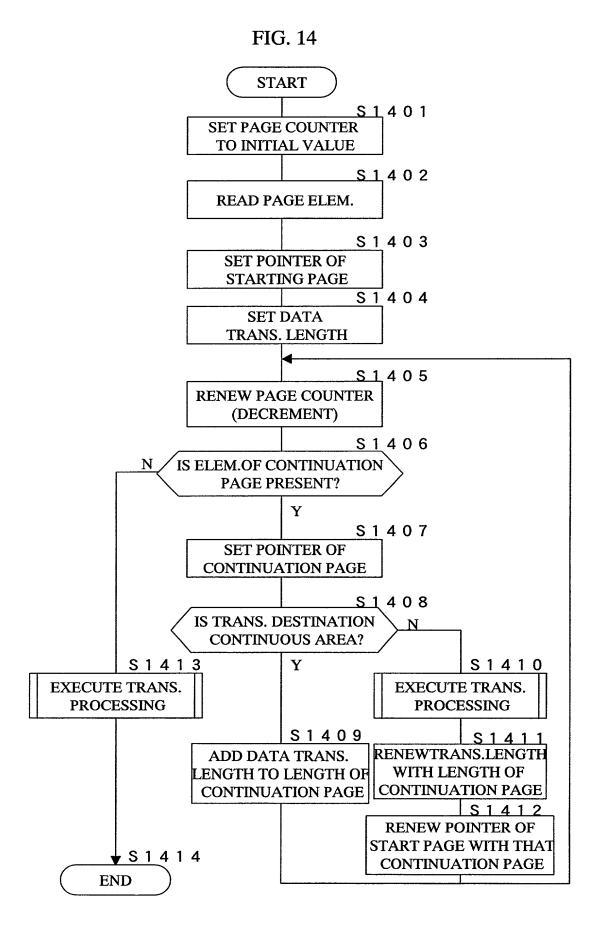


FIG. 15

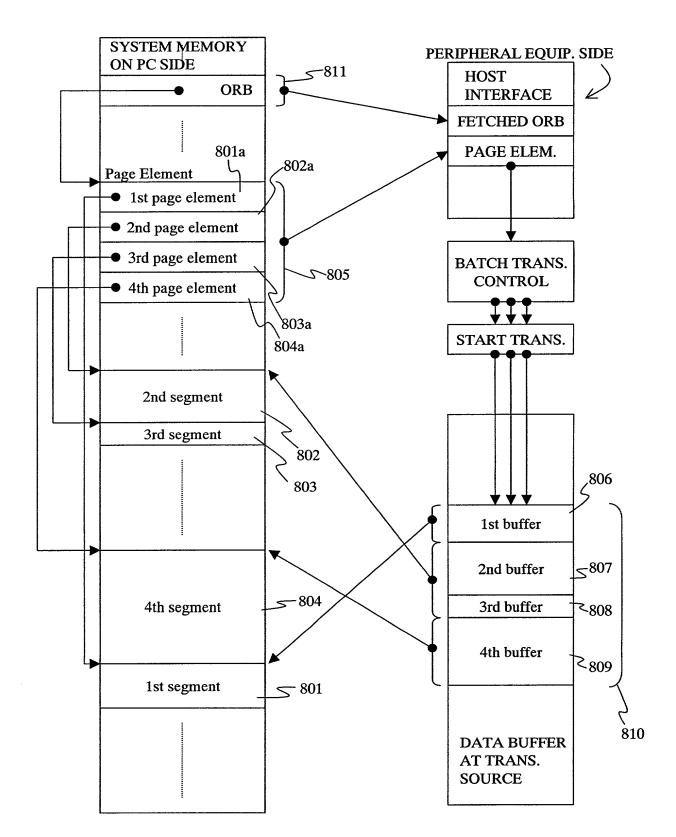


FIG. 16

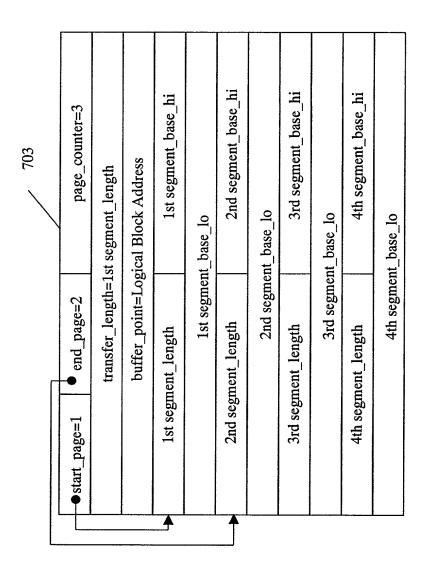


FIG. 1

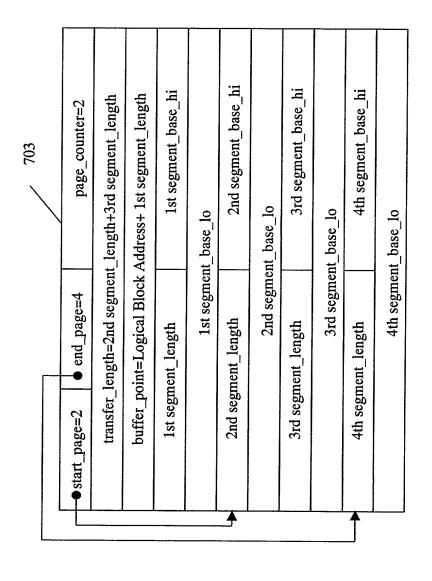
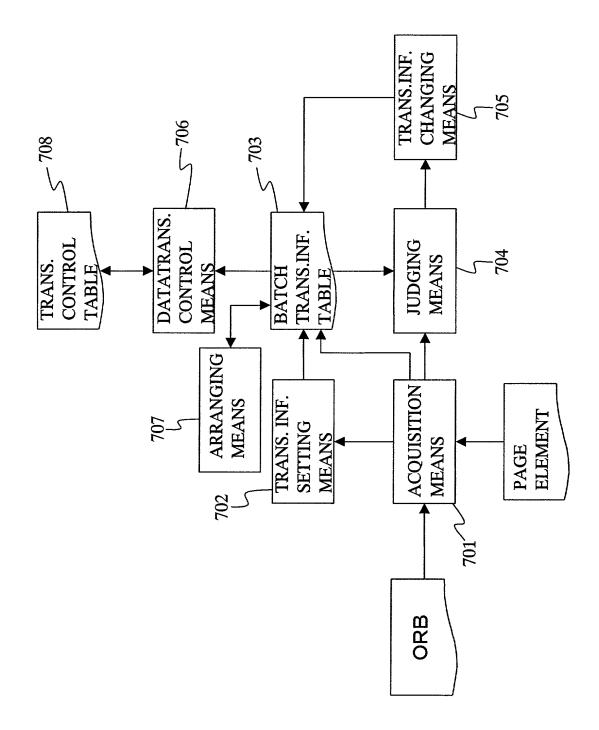


FIG. 18

703	page_counter	_length	1st segment_base_hi	t_base_lo	1st buffer_point	2nd segment_base_hi	nt_base_lo	2nd buffer_point	 nth segment_base_hi	ıt_base_lo	nth buffer_point
	end_page	transfer_length	1st segment_length	1st segment_base_lo	1st buff	2nd segment_length	2nd segment_base_lo	2nd buff	 nth segment_length	nth segment_base_lo	nth buff
	start_page		1st segr			2nd seg			nth seg		







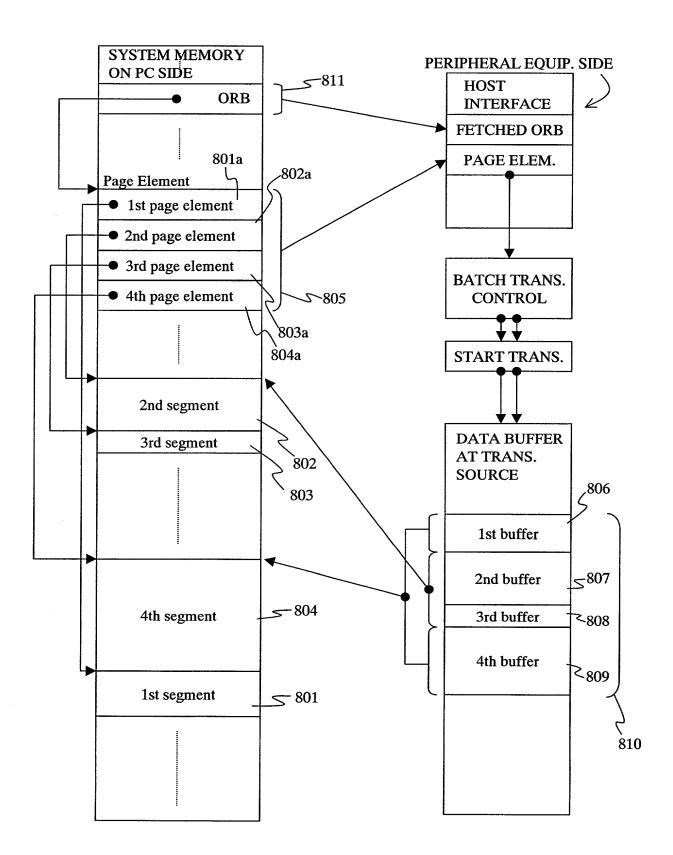


FIG. 21

703	page_counter=4	transfer_length=2nd segment_length	2nd segment_base_hi	2nd segment_base_lo	2nd buffer_point	3rd segment_base_hi	3rd segment_base_lo	3rd buffer_point	4th segment_base_hi	4th segment_base_lo	4th buffer_point	1st segment_base_hi	1st segment_base_lo	1st buffer_point
	end_page=0	transfer_length=	2nd segment_length	2nd segm	2nd bu	3rd segment_length	3rd segm	3rd bu	4th segment_length	4th segme	4th buf	1st segment_length	1st segme	1st bu
	• start_page=1		→ 2nd s			3rd s			4th se			1st se		

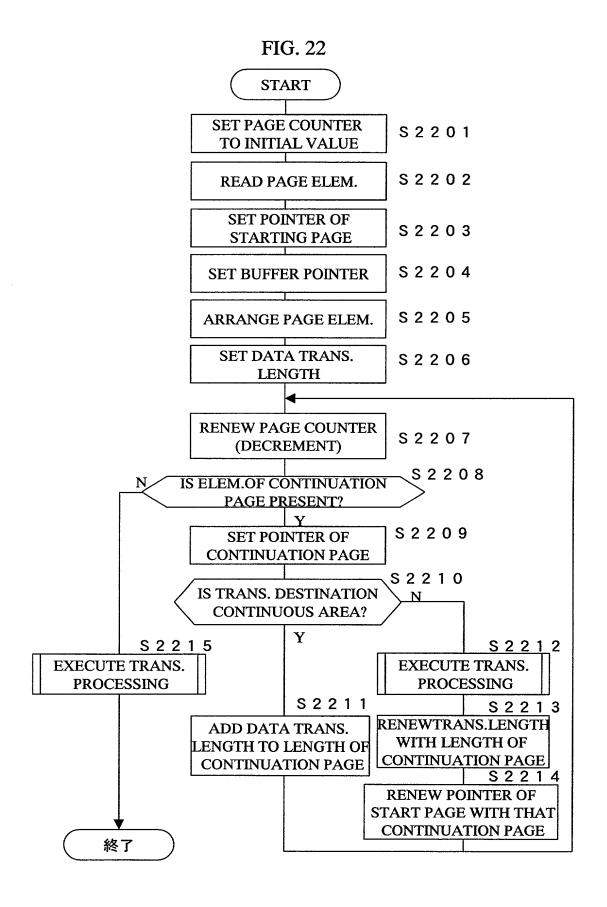


FIG. 23

703	page_counter=4	transfer_length=0	1st segment_base_hi	1st segment_base_lo	1st buffer_point=Logical Block Address	2nd segment_base_hi	2nd segment_base_lo	2nd buffer_point= 1st buffer_point+2nd segment_length	3rd segment_base_hi	3rd segment_base_lo	3rd buffer_point= 2nd buffer_point+3rd segment_length	4th segment_base_hi	4th segment_base_lo	4th buffer_point= 3rd buffer_point+4th segment_length
	end_page=0	transfer	1st segment_length	1st segme	1st buffer_point=I	2nd segment_length	2nd segme	iffer_point= 1st buf	3rd segment_length	3rd segme	ffer_point= 2nd buf	4th segment_length	4th segme	ffer_point= 3rd buff
	-•start_page=1		1st segn			2nd segi		2nd bu	3rd segn		3rd bu	4th segn		4th bul

FIG. 24

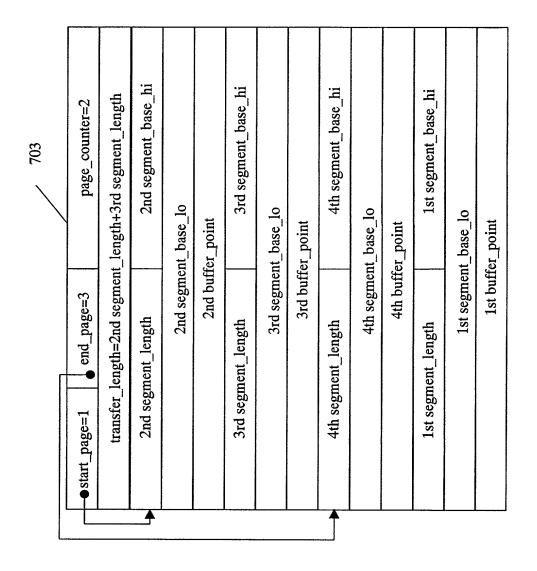


FIG. 25

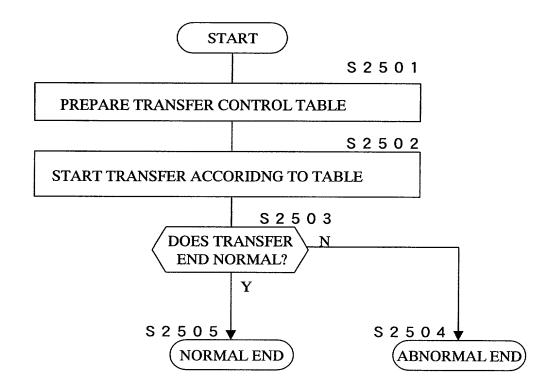


FIG. 26

708	start segment_base_hi	start segment_base_lo	buffer_length 2	 buffer_length n	buffer_address 1	buffer_address 2	buffer_address_n
	buffer_number	start segme	buffer_length 1	 buffer_length n-1	buffer_a	buffer_a	buffer a

FIG. 27

708	2nd segment_base_hi	t_base_lo	buffer_length_1=2nd segment_length buffer_length_2=3rd segment_length	buffer_address_1 = 2nd buffer_point	buffer_address_2 = 3rd buffer_point
	buffer_number = 2	2nd segment_base_lo	buffer_length_1=2nd segment_length	buffer_address_1	buffer address 2

FIG. 28

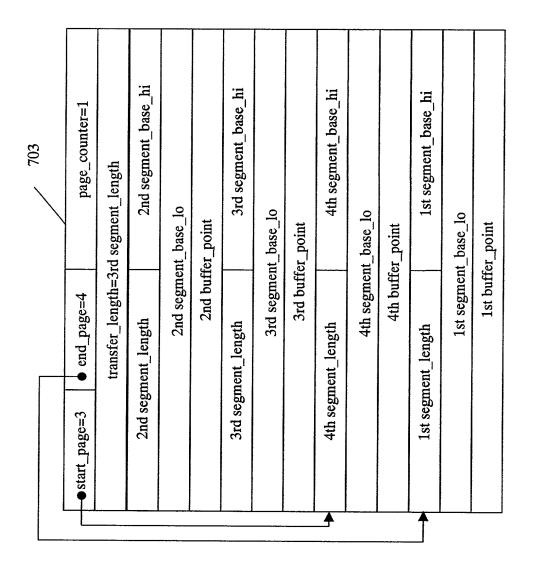
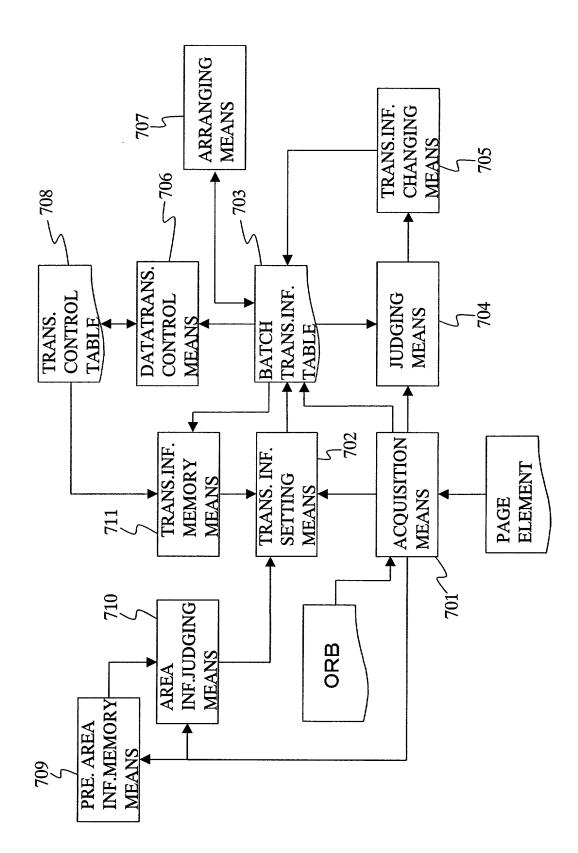


FIG. 29

Host	1st	2nd	3rd	Last1	Last
CD9050		1	1	1	1
186D000	14	15	15	15	15
186D000	13	14	14	14	14
186D000	15	16	16	16	16
1870000	12	13	13	13	13
1871000					
1872000					
1873000	11	12	12	12	12
1874000					
1875000	10	11	11	11	11
1876000	9	10	10	10	10
1877000					
1878000					
1879000					
187A000					
187B000	6	7	7	7	7
187C000					
187D000					
187E000	5	6	6	6	6
187F000	7	8	8	8	8
1880000	4	5	5	5	5
1881000	8	9	9	9	9
1882000	3	4	4	4	4
1883000					
1884000	2	3	3	3	3
1885000					
1886000		2	2	2	2
1886500	1				
1893000	16				

FIG. 30



3 (4)

